

01576 U.S. PTO  
07/28/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Suresh Marisetty et al.

Title: SYSTEM ABSTRACTION LAYER, PROCESSOR ABSTRACTION LAYER, AND OPERATING SYSTEM ERROR HANDLING (As Amended)

Attorney Docket No.: 884.205US2

PATENT APPLICATION TRANSMITTAL

**MAIL STOP PATENT APPLICATION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- ☒ Return postcard.
- ☒ **CONTINUATION** of prior Patent Application No. (09/475417) (under 37 CFR 1.53(b)) comprising:
  - ☒ Specification (23 pgs, including claims numbered 1 through 25 and a 1 page Abstract).
  - ☒ Formal Drawing(s) (5 sheets).
  - ☒ Copy of signed Combined Declaration and Power of Attorney (16 pgs) from prior application.
  - ☒ Incorporation by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*
  - ☒ Check in the amount of \$1818.00 to pay the filing fee.
- ☒ Prior application is assigned of record to Intel Corporation.
- ☒ Information Disclosure Statement ( 1 pgs), Form 1449 ( 1 pgs )  
References NOT enclosed, cited in prior application.
- ☒ Preliminary Amendment ( 14 pgs).

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	42-20	22	x 18.00 =	\$396.00
INDEPENDENT CLAIMS	11-5	8	x 84.00 =	\$672.00
[ ] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$750.00
TOTAL				\$1818.00

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This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.

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